

Fig 1
(PRIOR ART)

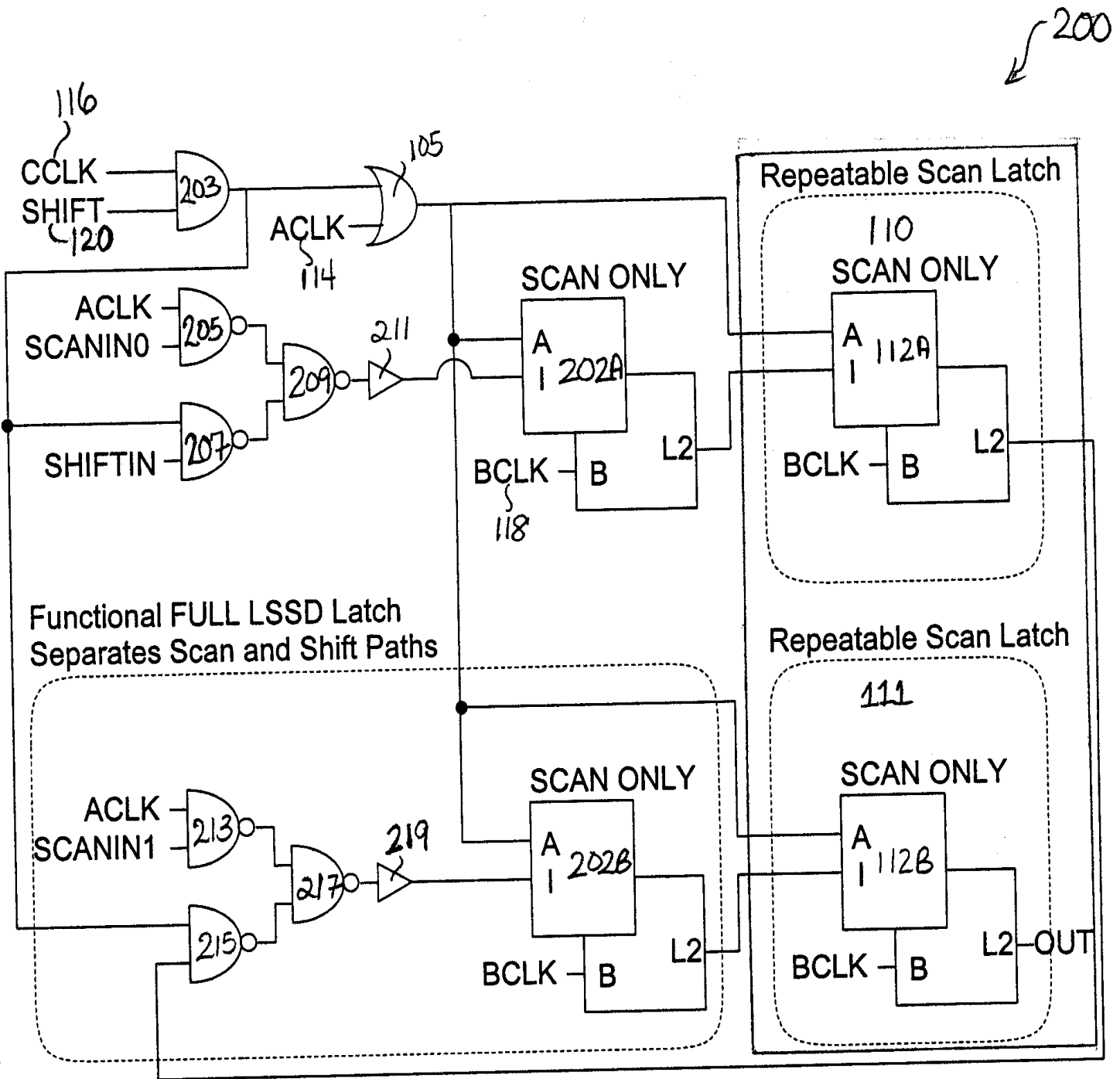
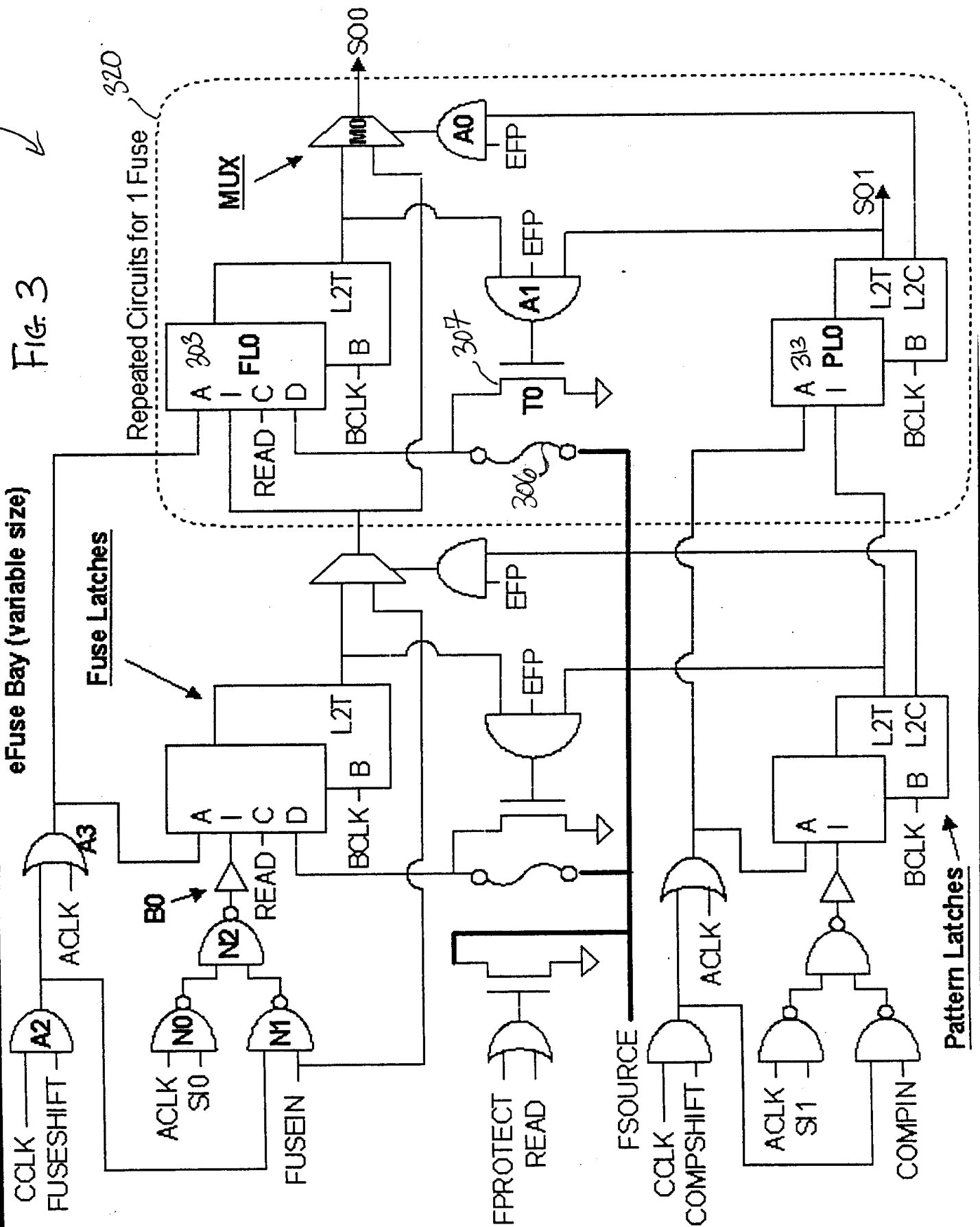


FIGURE 2

eFuse Bay (variable size)

Fig. 3



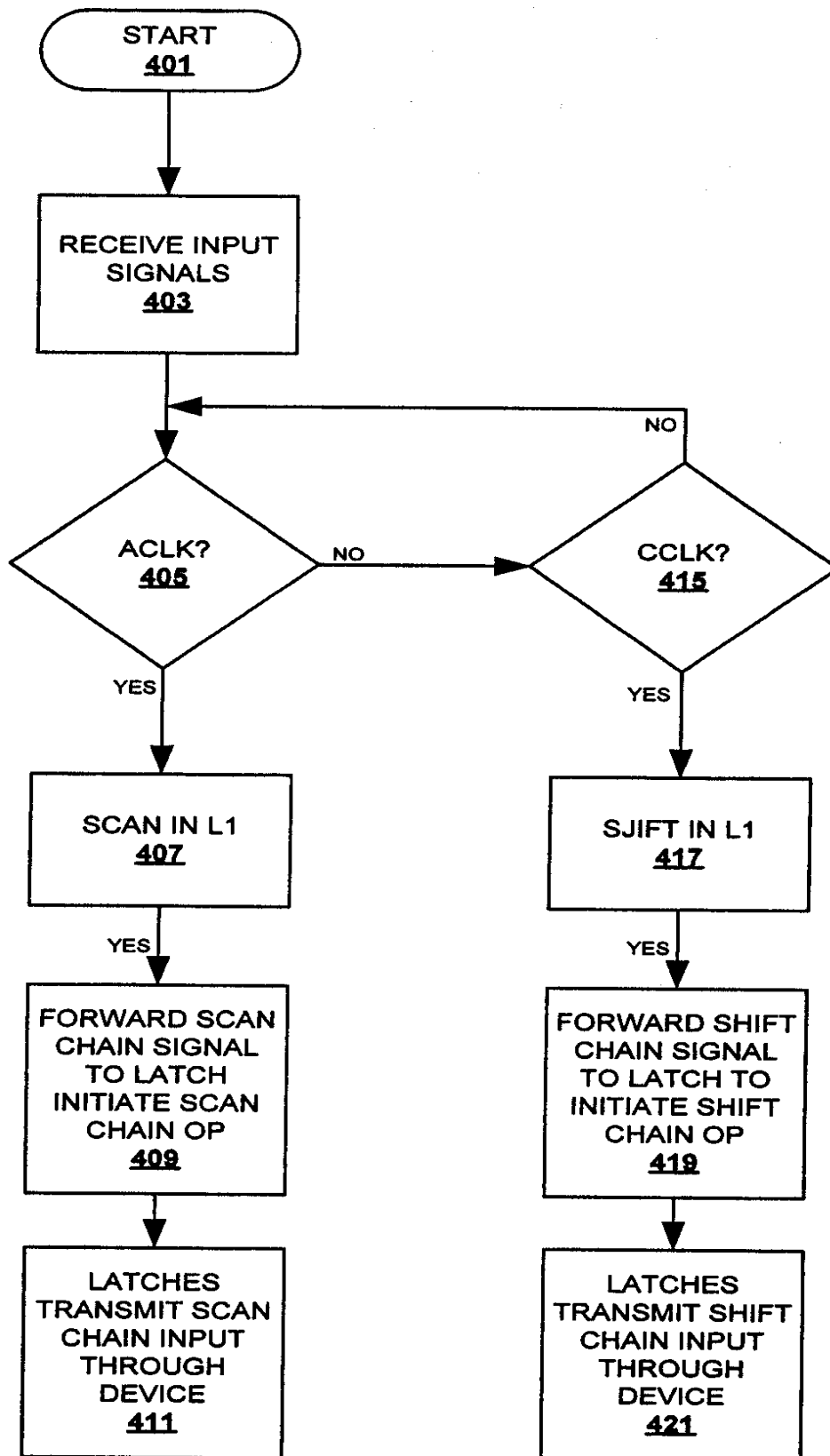


FIGURE 4